

Fig. 1

PRIOR ART

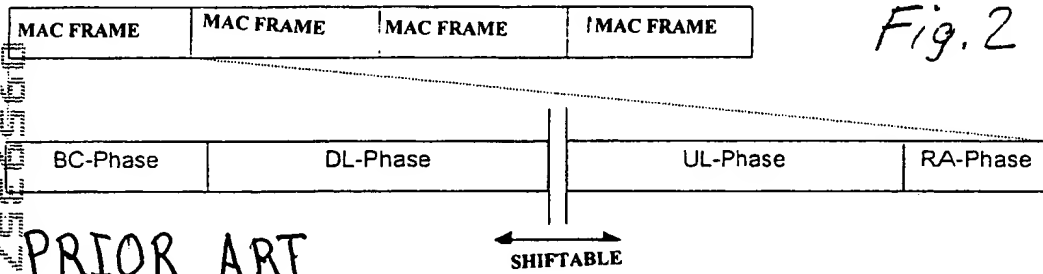


Fig. 2

PRIOR ART

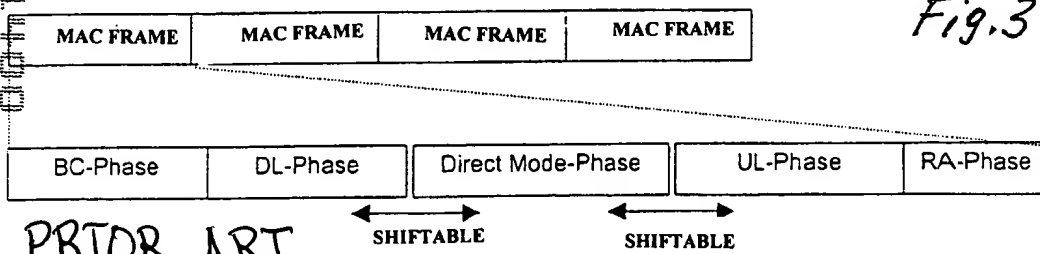


Fig. 3

PRIOR ART

Fig. 4 PRIOR ART

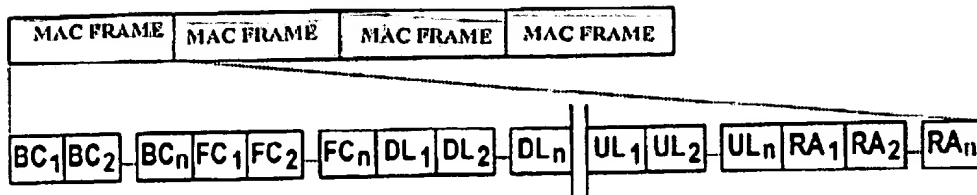


Fig. 5

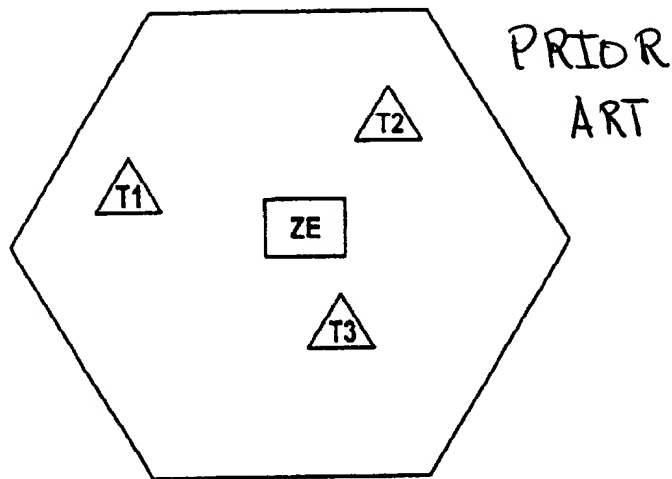


Fig. 6

		MAC-R 1		MAC-R 2		MAC-R 3		MAC-R 4	
		RG	RR	RG	RR	RG	RR	RG	RR
Terminal 1	SV	1	2	2	0	0	1	1	0
	NV	36	1000	33	980	14	1005	13	1025
	R-bit	-	0	-	1	-	1	-	1
Terminal 2	SV	0	1	1	0	0	0	0	0
	NV	2	25	2	24	5	20	5	15
	R-bit	-	0	-	0	-	0	-	0
Terminal 3	SV	0	0	0	0	0	0	0	0
	NV	4	100	4	96	12	84	13	71
	R-bit	-	0	-	0	-	0	-	0

Fig. 7

		MAC-R 100		MAC-R 101		MAC-R 102		MAC-R 103	
		RG	RR	RG	RR	RG	RR	RG	RR
Terminal 1	SV	0	1	1	0	0	1	1	0
	NV	14	1020	13	980	28	952	25	927
	R-bit	-	1	-	0	-	0	-	0
Terminal 2	SV	0	1	1	0	0	0	0	0
	NV	0	0	0	24	3	21	2	19
	R-bit	-	0	-	0	-	0	-	0
Terminal 3	SV	0	0	0	0	0	1	1	0
	NV	15	35	35	0	0	0	0	0
	R-bit	-	0	-	0	-	0	-	0

Fig. 8

